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Applicant: Sanjay Anand, et al.

Atty Docket No.: 13768.69.1.1

Sheet 1 of 3

Group: Unknown

For:

METHOD AND COMPUTER PROGRAM PRODUCT FOR
OFFLOADING PROCESSING TASKS FROM SOFTWARE TO
HARDWARE

11/29/00
JC760 U.S. Pro
09/726062

INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

Examiner <u>Initial*</u>	Patent <u>Number</u>	Issue <u>Date</u>	Name	<u>Class</u>	<u>Sub Class</u>
A10	A1. 5,410,696	04/1995	Seki et al.	395	650
	A2. 5,504,903	04/1996	Chen et al.	395	700
	A3. 5,621,894	04/1997	Menezes et al.	395	200.12
	A4. 5,634,070	05/27/97	Robinson	395	800
	A5. 5,678,021	10/1997	Pawate et al.	395	431
	A6. 5,717,691	02/10/98	Dighe et al.	370	401
	A7. 5,737,520	04/1998	Gronlund et al.	395	183.15
	A8. 5,889,848	03/1999	Cookson	379	230
A10	A9. 5,913,028	06/1999	Wang et al.	395	200.33

Examiner:

DR

Date Considered: 6/1/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Foreign Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Publ. Date</u>	<u>Country or Patent Office</u>	<u>Class</u>	<u>Sub Class</u>	<u>Trans-lation</u>
<u>AIC</u>	A10. 0778523	06/11/97	EP	---	---	Yes

Other Documents

(including author (if listed), title, relevant pages, date of publication including at least month and year).

Examiner

Initial*

AIC A11. Gott, Robert A., "Intelligent I/O Eases Subsystem Development," *Computer Design*, Vol. 37, No. 5, pp. 106, 108-110, May 1, 1998.

Examiner: 

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